

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383001		AAYUSHI MANDLOI		SHOBHANA		,71415625G		,MITCOE		,B120383001							
SEM.:1						SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	28	48	P	404189	MOBILE COMMUNICATION	PP	100	40	21	33	54	P
404182	COMPUTER NETWORKS	PP	100	40	07	33	40	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	37	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	28	40	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	40	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	28	44	P	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	23	48	71	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	40	59	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			30	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C	404195	PROJECT PHASE II	TW	100	40			89	P
404188	PROJECT PHASE I	OR	50	20			42	P C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 907/1500 , RESULT:FIRST CLASS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120383002		ABHISHEK KUMAR		SUDHA DEVI		,71541691L		,MITCOE		,B120383002							
SEM.:1						SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	42	67	P C	404189	MOBILE COMMUNICATION	PP	100	40	19	39	58	P
404182	COMPUTER NETWORKS	PP	100	40	13	28	41	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	40	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	37	53	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	44	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	44	60	P C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	27	44	71	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	44	61	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			47	P C	404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 1026/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383003		ADYA AMRUTA MURALIDHAR		MADHAVI		,71505688D		,MITCOE		,B120383003							
SEM.:1						SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	40	53	P C	404189	MOBILE COMMUNICATION	PP	100	40	13	40	53	P
404182	COMPUTER NETWORKS	PP	100	40	06	36	42	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	43	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	09	35	44	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	43	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	42	58	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	14	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	35	52	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P C	404195	PROJECT PHASE II	TW	100	40			86	P
404188	PROJECT PHASE I	OR	50	20			40	P C	404195	PROJECT PHASE II	PR	50	20			38	P

GRAND TOTAL = --/1500 , RESULT:FAILS

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B120383004 AHIRE HARSHADA SANJIV UJWALA ,71639110E ,MITCOE ,B120383004

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	51	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	28	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	39	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	45	63	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	42	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	45	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	42	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	52	70	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	29	44	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			78	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 992/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383005 AKASH VIJAYKUMAR PASARI MAMTA ,71541710L ,MITCOE ,B120383005

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	50	72	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	37	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	36	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	48	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	24	49	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	45	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	48	74	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	44	62	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	45	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1126/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383006 ARSHIA AIMAN ALI SHABANA KHATOON ,71541740B ,MITCOE ,B120383006

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	28	44	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	30	43	P	
404183	MICROWAVE ENGINEERING	PP	100	40	12	36	48	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	44	59	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	40	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	42	60	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	33	50	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	38	50	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	16	30	46	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			28	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 894 + 06/1500 , RESULT:FIRST CLASS [0.2]

RESULT RESERVED FOR BACKLOGS.

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B120383007 ASHISH LONKAR VEENA ,71541743G ,MITCOE ,B120383007

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	46	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	41	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	46	74	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	57	85	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	24	48	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			46	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	48	72	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	48	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	54	75	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	29	46	75	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			46	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	P
404195	PROJECT PHASE II	TW	100	40			97	P	P
404195	PROJECT PHASE II	PR	50	20			48	P	P

GRAND TOTAL = 1211/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383008 ASHVINI KUMAR SHESHA ,71541745C ,MITCOE ,B120383008

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	29	43	P	C
404182	COMPUTER NETWORKS	PP	100	40	07	40	47	P	
404183	MICROWAVE ENGINEERING	PP	100	40	16	34	50	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	44	61	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	47	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	41	59	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	42	55	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	41	58	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	44	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 956/1500 , RESULT:FIRST CLASS

B120383009 AVIN SHARMA MEENAKSHI ,71541752F ,MITCOE ,B120383009

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	35	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	46	67	P	
404183	MICROWAVE ENGINEERING	PP	100	40	15	29	44	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	48	72	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	47	70	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	37	57	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	40	56	P	P
404191D	SOFT COMPUTING	PP	100	40	18	54	72	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	43	60	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1015/1500 , RESULT:FIRST CLASS WITH DISTINCTION

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B120383010 AWASTHI ABHISHEK BHARAT ULKA ,71541753D ,MITCOE ,B120383010

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	38	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	09	31	40	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	39	58	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	37	58	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	46	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	38	58	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	43	59	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	33	51	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	33	46	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404195	PROJECT PHASE II	TW	100	40			93	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 992/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383011 BADADHE MANSI BABASAHEB SULBHA ,71541757G ,MITCOE ,B120383011

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	37	59	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	28	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	35	54	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	52	75	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	51	76	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	44	62	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	41	53	P	P
404191D	SOFT COMPUTING	PP	100	40	19	46	65	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	23	53	76	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1022/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383012 BANGARE PALLAVI TULSHIDAS SANDHYA ,71541768B ,MITCOE ,B120383012

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	44	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	09	32	41	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	35	56	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	41	62	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	23	42	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	53	70	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	44	65	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	46	61	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	15	51	66	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1031/1500 , RESULT:FIRST CLASS WITH DISTINCTION

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B120383013 BANSODE PRATIK DADASAHEB TEJASWINI ,71541769L ,MITCOE ,B120383013

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	35	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	33	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	39	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	54	76	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	43	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	48	74	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	46	71	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	46	61	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	38	59	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			84	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1030/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383014 BHALSHANKAR BHARAT ROHIDAS SATYABHAMA ,71541781K ,MITCOE ,B120383014

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	38	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	09	37	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	39	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	51	70	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	47	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	51	75	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	36	57	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	41	63	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	48	67	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			97	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1068/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383015 BHAMRE SHYAMAL MADHUKAR SHAILJA ,71639111C ,MITCOE ,B120383015

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	48	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	30	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	39	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	44	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	24	45	69	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	54	73	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	46	65	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	51	74	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	24	49	73	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			93	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1089/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383016 BHARGAVE OJAS SACHIN SUPRIYA ,71541783F ,MITCOE ,B120383016

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	26	47	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	37	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	42	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	50	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	55	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	48	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	45	69	P	
404191D	SOFT COMPUTING	PP	100	40	27	53	80	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	52	78	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1157/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383017 BHAT AMEY BHARATKUMAR MAYA ,71541785B ,MITCOE ,B120383017

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	28	50	78	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	40	61	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	53	80	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	25	54	79	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	58	84	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	29	44	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	26	48	74	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	19	40	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			46	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 1187/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383018 BHUJBAL PRASHANT DILIP VIDYA ,71541795K ,MITCOE ,B120383018

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	45	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	48	60	P	
404183	MICROWAVE ENGINEERING	PP	100	40	19	37	56	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	40	53	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	49	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	48	70	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	35	57	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	44	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	44	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383019 BHUTE VAIBHAV PUNDLIK BAKULA ,71639112M ,MITCOE ,B120383019

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	39	53	P	C	404189	MOBILE COMMUNICATION	PP	100	40	12	44	56	P
404182	COMPUTER NETWORKS	PP	100	40	11	36	47	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	43	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	38	54	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	42	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	49	57	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	28	41	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	13	40	53	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			27	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C	404195	PROJECT PHASE II	TW	100	40			90	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			39	P

GRAND TOTAL = 929/1500 , RESULT:FIRST CLASS

B120383020 BRAJESH KUMAR RASO ,71541809C ,MITCOE ,B120383020

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	37	53	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P
404182	COMPUTER NETWORKS	PP	100	40	12	28	40	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	37	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	35	51	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	39	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	44	59	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	31	43	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	40	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			24	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C	404195	PROJECT PHASE II	TW	100	40			90	P
404188	PROJECT PHASE I	OR	50	20			47	P	C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 918/1500 , RESULT:FIRST CLASS

B120383021 BUDDHA SUMANJEETH SREENIVASRAO JAYASUDHA ,71541810G ,MITCOE ,B120383021

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	44	60	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	40	53	P
404182	COMPUTER NETWORKS	PP	100	40	08	40	48	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	37	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	34	52	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	39	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	46	65	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	38	56	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	49	66	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			85	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 972/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383022 BURADE AMAR VINOD MADHURI ,71542347K ,MITCOE ,B120383022

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	43	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	28	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	28	50	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	47	64	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	46	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	40	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	46	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	43	62	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	37	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			86	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 975/1500 , RESULT:FIRST CLASS

B120383023 CHANDGUDE PRACHI ABASAHEB SANGITA ,71639113K ,MITCOE ,B120383023

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	47	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	46	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	53	75	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	43	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	45	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	51	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	48	70	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	40	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1076/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383024 CHARJAN VAISHNAVI ABHAY SUCHITA ,71639114H ,MITCOE ,B120383024

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	46	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	38	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	53	75	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	45	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	49	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	52	71	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	41	58	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1075/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383025 CHATARWAD SHIVKUMAR SANJAY VANDANA ,71541817D ,MITCOE ,B120383025

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	46	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	31	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	47	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	54	79	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	48	69	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	54	70	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	47	65	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	42	58	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	54	80	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1077/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383026 CHAUDHARY AJAY YOGESH GITA ,71315583D ,MITCOE ,B120383026

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	16	--	F	
404182	COMPUTER NETWORKS	PP	100	40	07	29	--	F	
404183	MICROWAVE ENGINEERING	PP	100	40	06	15	--	F	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	18	--	F	
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	39	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			29	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	41	57	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	31	43	P	P
404191D	SOFT COMPUTING	PP	100	40	12	28	40	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	36	48	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			24	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			21	P	P
404195	PROJECT PHASE II	TW	100	40			80	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120383027 CHAVHAN YOGITA AMARSING VANITA ,71639115F ,MITCOE ,B120383027

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	42	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	54	71	P	
404183	MICROWAVE ENGINEERING	PP	100	40	25	39	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	49	68	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	44	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	52	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	48	74	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	47	64	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	50	70	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1105/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383028 CHOUGULE AKSHAY BHARATKUMAR SULABHA ,71541831K ,MITCOE ,B120383028

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	38	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	36	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	36	53	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	32	54	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	43	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	41	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	46	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	41	58	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	38	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 980/1500 , RESULT:FIRST CLASS

B120383029 DAGADE SHUBHANGI KUNDLIK BHARATI ,71639116D ,MITCOE ,B120383029

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	46	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	45	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	43	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	53	70	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	23	48	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	52	67	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	39	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	45	66	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	41	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			29	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1042/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383030 DAHALE PRIYA SANDEEP JAYA ,71639117B ,MITCOE ,B120383030

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	52	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	29	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	43	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	39	60	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	46	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	43	60	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	39	55	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	42	61	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	31	44	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1007/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383031 DARANDALE LAXMI BHASKAR SHANTA ,71639118L ,MITCOE ,B120383031

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	50	74	P	C	404189	MOBILE COMMUNICATION	PP	100	40	20	43	63	P	P
404182	COMPUTER NETWORKS	PP	100	40	22	43	65	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	48	71	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	48	69	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	48	70	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	56	76	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	39	54	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	46	66	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1076/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383032 DASHPUTRE SWAPNIL HARICHANDRA KALPANA ,71315604L ,MITCOE ,B120383032

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	16	--	F		404189	MOBILE COMMUNICATION	PP	100	40	18	28	46	P	P
404182	COMPUTER NETWORKS	PP	100	40	08	28	--	F		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	24	--	F	F
404183	MICROWAVE ENGINEERING	PP	100	40	12	53	65	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	39	53	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	35	50	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	09	33	42	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	47	64	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			25	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			21	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P	C	404195	PROJECT PHASE II	TW	100	40			87	P	P
404188	PROJECT PHASE I	OR	50	20			37	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120383033 DATE ISHANI CHETAN SHARMILA ,71541838G ,MITCOE ,B120383033

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	38	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	36	51	P	P
404182	COMPUTER NETWORKS	PP	100	40	14	39	53	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	32	47	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	44	57	P	C	404191D	SOFT COMPUTING	PP	100	40	17	40	57	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	36	55	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	37	55	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	46	65	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			25	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			30	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			28	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C	404195	PROJECT PHASE II	TW	100	40			88	P	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 892 + 08/1500 , RESULT:FIRST CLASS [0.2]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383034 DEEPAK KUMAR NABITA ,71541846H ,MITCOE ,B120383034

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	38	59	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	31	43	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	45	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	49	73	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	49	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	36	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	47	68	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	43	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	33	48	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			27	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1003/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383035 DESHMUKH PRANALI PRAKASH KANCHAN ,71541855G ,MITCOE ,B120383035

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	29	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	47	64	P	
404183	MICROWAVE ENGINEERING	PP	100	40	23	35	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	40	67	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	51	66	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	34	47	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	46	69	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	41	57	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	41	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1030/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383036 DESHMUKH SHUBHAM NISHIKANT PALLAVI ,71541856E ,MITCOE ,B120383036

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	28	45	73	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	52	76	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	29	63	92	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	49	77	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	54	76	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	54	81	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	29	51	80	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	57	80	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	27	53	80	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1254/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383037 DESHPANDE RAKSHANDA RAMRAGHAV APARNA ,71538393M ,MITCOE ,B120383037

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	41	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	59	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	50	74	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	50	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	48	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	58	81	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	53	72	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1104/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383038 DHAKATE MONIKA KRUSHNARAO RUCHIKA ,71541861M ,MITCOE ,B120383038

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	38	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	41	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	38	59	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	48	74	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	49	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	42	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	48	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	51	74	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	37	51	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1064/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383039 DHANDE BHAVESH SATISH SUPRIYA ,71639119J ,MITCOE ,B120383039

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	36	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	30	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	52	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	41	65	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	43	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	49	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	52	73	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	38	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1099/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383040 DHILE MANISH VIJAY JAYASHRI DHILE ,71415775K ,MITCOE ,B120383040

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	28	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	31	44	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	47	68	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	34	57	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	44	60	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	48	67	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	42	57	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	32	45	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	09	40	49	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			88	P	
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 941/1500 , RESULT:FIRST CLASS

B120383041 DHUPAR GURSHISH SINGH MANJIT SINGH GURMEET KAUR ,71541869G ,MITCOE ,B120383041

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	36	54	P	C	404189	MOBILE COMMUNICATION	PP	100	40	16	38	54	P	P
404182	COMPUTER NETWORKS	PP	100	40	16	28	44	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	43	57	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	43	64	P	C	404191D	SOFT COMPUTING	PP	100	40	21	43	64	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	47	65	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	23	38	61	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	50	73	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			96	P	
404188	PROJECT PHASE I	OR	50	20			47	P	C	404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1027/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383042 DIDWANIYA ANIKET DILIP SANGITA ,71639120B ,MITCOE ,B120383042

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	40	57	P	C	404189	MOBILE COMMUNICATION	PP	100	40	19	53	72	P	P
404182	COMPUTER NETWORKS	PP	100	40	09	41	50	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	49	72	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	47	60	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	49	71	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	46	61	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	25	42	67	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	42	60	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C	404195	PROJECT PHASE II	TW	100	40			97	P	
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1072/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383043 DIKSHA MILIND RANPISE RAJASHREE ,71541871J ,MITCOE ,B120383043

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	32	48	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	30	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	43	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	43	61	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	34	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	40	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	40	57	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	42	57	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	37	63	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1012/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383044 DILSHAD AHMAD ARZOO BEGAM ,71541873E ,MITCOE ,B120383044

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	37	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	35	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	55	77	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	50	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	44	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	46	70	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	49	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	54	77	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	42	64	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1130/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383045 DOIFODE RAJESHWARI RAJENDRA ASHA ,71639121L ,MITCOE ,B120383045

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	32	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	57	78	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	46	61	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	38	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	47	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	51	74	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	52	72	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	31	46	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			77	P	
404195	PROJECT PHASE II	PR	50	20			35	P	

GRAND TOTAL = 1017/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383047 DURGE APURVA HARISHCHANDRA MANISHA ,71214106F ,MITCOE ,B120383047

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	34	47	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	36	49	P	
404183	MICROWAVE ENGINEERING	PP	100	40	19	28	47	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	41	55	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	36	51	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	28\$	48	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	42	57	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	44	59	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	07	33\$	40	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 894/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

B120383048 EKAL SUJAY SHIVAJI ANJNA ,71639123G ,MITCOE ,B120383048

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	44	60	P	
404183	MICROWAVE ENGINEERING	PP	100	40	23	53	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	44	64	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	36	54	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	49	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	49	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	49	67	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	34	47	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1062/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383049 FULMALI RASHMI RAJESH VANDANA ,71541880H ,MITCOE ,B120383049

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	28	42	P	
404182	COMPUTER NETWORKS	PP	100	40	09	33	42	P	
404183	MICROWAVE ENGINEERING	PP	100	40	12	33	45	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	43	60	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	36	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	32	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	38	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	42	56	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	29	41	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 945/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383050 GAIKWAD AKASH KRISHNA PARVATI ,71541888C ,MITCOE ,B120383050

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	35	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	58	78	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	50	75	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	42	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	42	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	50	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	55	74	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	21	45	66	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1108/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383051 GAIKWAD VIDYA PRAFULL DAIVATA ,71639124E ,MITCOE ,B120383051

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	40	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	40	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	54	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	48	60	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	45	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	32	50	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	53	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	48	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	38	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1024/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383052 GANGURDE ROSHAN BALKRISHNA RATNA ,71345577C ,MITCOE ,B120383052

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	16	--	F	
404182	COMPUTER NETWORKS	PP	100	40	06	34\$	40	P	
404183	MICROWAVE ENGINEERING	PP	100	40	09	40	49	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	12	33	45	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	12	34	46	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	30	42	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	35	50	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	39	52	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	32	44	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			87	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383053 GARUD SHAMAL SURYAKANT SANGITA ,71639125C ,MITCOE ,B120383053

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	37	48	P
404182	COMPUTER NETWORKS	PP	100	40	10	30	40	P C
404183	MICROWAVE ENGINEERING	PP	100	40	14	37	51	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	46	54	P C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	37	54	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P C
404188	PROJECT PHASE I	OR	50	20			39	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	49	61	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	50	65	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	50	72	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	49	67	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P
404195	PROJECT PHASE II	TW	100	40			93	P
404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 1001/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383054 GAWADE BAPU MACCHINDRA SHARADA ,71315662H ,MITCOE ,B120383054

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	32	44	P C
404182	COMPUTER NETWORKS	PP	100	40	07	33	40	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	36	56	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	36	53	P C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	34	51	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P C
404188	PROJECT PHASE I	OR	50	20			39	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	33	53	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	40	58	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	46	63	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	38	51	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404195	PROJECT PHASE II	TW	100	40			88	P
404195	PROJECT PHASE II	PR	50	20			37	P

GRAND TOTAL = 929/1500 , RESULT:FIRST CLASS

B120383055 GHATNEKAR SAURABH GANESH MANASI ,71639126M ,MITCOE ,B120383055

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	39	59	P C
404182	COMPUTER NETWORKS	PP	100	40	22	37	59	P C
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	51	78	P C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	52	78	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P C
404188	PROJECT PHASE I	OR	50	20			46	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	45	61	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	49	75	P
404191D	SOFT COMPUTING	PP	100	40	27	57	84	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	43	63	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P
404195	PROJECT PHASE II	TW	100	40			92	P
404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 1141/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383056 GODBOLE SAURABH SUNIL MADHAVI ,71541914F ,MITCOE ,B120383056

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	33	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	36	61	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	51	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	51	74	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	46	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	38	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	49	74	P	
404191D	SOFT COMPUTING	PP	100	40	18	55	73	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	46	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383057 GOSAVI APURVA YASHWANT MANDA ,71639127K ,MITCOE ,B120383057

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	33	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	08	38	46	P	
404183	MICROWAVE ENGINEERING	PP	100	40	14	47	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	48	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	39	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	45	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	48	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	51	70	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	15	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

B120383058 GUPTA SANJIV SHIVKUMAR GEETA ,71639128H ,MITCOE ,B120383058

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	29	43	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	30	41	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	45	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	49	70	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	44	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	43	67	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	43	66	P	
404191D	SOFT COMPUTING	PP	100	40	22	50	72	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	42	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1007/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383059 INGLE SAGAR KADUBA KAMALBAI ,71541940E ,MITCOE ,B120383059

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	32	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	31	45	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	43	59	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	23	48	71	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	43	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	45	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	38	63	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	46	63	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	44	64	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1044/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383060 JAISWAL MAHESH SANJAY KAVITA ,71541960K ,MITCOE ,B120383060

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	33	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	40	52	P	
404183	MICROWAVE ENGINEERING	PP	100	40	23	30	53	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	42	55	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	39	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	38	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	44	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	49	67	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	43	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 993/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383061 JATKAR CHIRANJIV BHIMAPPA BHAGYASHRI ,71541963D ,MITCOE ,B120383061

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	50	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	47	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	47	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	37	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	42	62	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	47	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	44	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	38	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1059/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383062 JAWANJAL SUMIT PRADIP VANDANA ,71639129F ,MITCOE ,B120383062

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	57	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	46	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	35	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	52	70	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	41	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	36	51	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	51	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	54	72	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	28	40	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	
404195	PROJECT PHASE II	TW	100	40			77	P	
404195	PROJECT PHASE II	PR	50	20			35	P	

GRAND TOTAL = 983/1500 , RESULT:FIRST CLASS

B120383063 JAWARE GAURAV GANESH PUSHPA ,71639130K ,MITCOE ,B120383063

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	51	72	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	42	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	40	60	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	47	65	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	39	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	45	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	50	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	44	63	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	36	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1067/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383064 JOSHI ANEESH DHIRENDRA RADHIKA ,71541965L ,MITCOE ,B120383064

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	55	78	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	46	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	44	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	46	65	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	24	44	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	48	72	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	51	74	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	42	60	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	25	53	78	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1156/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383065 JOSHI CHINMAYEE DHANANJAY NEHA ,71541966J ,MITCOE ,B120383065

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	45	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	28	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	43	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	47	68	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	46	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	36	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	47	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	44	58	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	21	49	70	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1114/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383066 JUNIA M SANGMA SAIPARI SAILO ,71315722E ,MITCOE ,B120383066

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	35	47	P	C
404182	COMPUTER NETWORKS	PP	100	40	08	32	40	P	
404183	MICROWAVE ENGINEERING	PP	100	40	16	28	44	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	39	50	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	39	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	
404188	PROJECT PHASE I	OR	50	20			28	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	30	49	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	31	54	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	46	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	11	17	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	
404195	PROJECT PHASE II	TW	100	40			75	P	
404195	PROJECT PHASE II	PR	50	20			36	P	

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

B120383067 KADALE PARAG GULABRAO VANDANA ,71415879J ,MITCOE ,B120383067

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	06	35	41	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	31	43	P	
404183	MICROWAVE ENGINEERING	PP	100	40	14	36	50	P	
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	12	37	49	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	28	42	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			27	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	33	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	28	44	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	33	48	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	10	25	35	#	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 822/1500 , RESULT:SECOND CLASS [# 0.4]

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383068 KAMBLE MANISHA SUBHASH SUSHILA ,71505709L ,MITCOE ,B120383068

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	42	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	10	37	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	04	37	41	P	
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	08	40	48	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	12	48	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	19	--	F	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	13	--	F	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	31	52	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	15	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			86	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

B120383069 KARAMBELKAR ABHISHEK MOHAN SUPRIYA ,71541985E ,MITCOE ,B120383069

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	46	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	38	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	29	51	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	50	75	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	49	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	43	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	37	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	41	60	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	50	69	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1088/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383070 KATKADE NISHIGANDHA NAMDEV SHITAL ,71639131H ,MITCOE ,B120383070

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	47	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	37	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	42	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	48	68	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	31	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	47	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	36	58	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	48	66	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	33	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1018/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383071 KATYARMAL MANISH MANOHARRAO VARSHA ,71639132F ,MITCOE ,B120383071

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	36	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	36	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	40	60	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	45	56	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	37	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	47	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	36	59	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	42	58	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	28	47	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 981/1500 , RESULT:FIRST CLASS

B120383072 KHEDEKAR SHRIYA RAMESH RASIKA ,71542006C ,MITCOE ,B120383072

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	49	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	44	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	51	78	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	55	79	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	43	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	45	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	37	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	27	42	69	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	34	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1146/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383073 KOKATE KEWAL BHARAT ANURADHA ,71639134B ,MITCOE ,B120383073

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	49	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	38	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	38	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	42	59	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	43	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	43	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	40	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	50	72	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	40	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1037/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383074 KOTHAWADE YASH GANESH SEEMA ,71542014D ,MITCOE ,B120383074

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	52	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	37	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	40	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	46	68	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	50	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	51	74	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	42	62	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	47	68	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	44	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383075 KULKARNI ABHISHEK ABHAY VAISHALI ,71542020J ,MITCOE ,B120383075

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	28	55	83	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	50	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	56	84	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	58	83	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	25	55	80	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			47	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			47	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	56	76	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	41	69	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	26	60	86	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	26	52	78	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			46	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			46	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1273/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383076 KULKARNI INDRAJEET ATUL SUNITA ,71542023C ,MITCOE ,B120383076

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	53	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	44	69	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	50	69	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	50	75	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	43	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	40	58	P	
404191D	SOFT COMPUTING	PP	100	40	20	52	72	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	45	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383077 KULKARNI VEDASHREE UDAY SHARVARI ,71542029B ,MITCOE ,B120383077

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	46	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	30	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	32	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	47	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	48	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	41	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	44	62	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	42	65	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	15	37	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1025/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383078 KUMAR SHUBHAM SUNITA ,71415943D ,MITCOE ,B120383078

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	46	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	28	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	29	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	29	43	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	40	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			31	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	10	40	50	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	40	52	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	10	--	F	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	31	44	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			28	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120383079 KUNAL KAMPASSI RITA ,71542032B ,MITCOE ,B120383079

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	46	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	39	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	42	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	48	71	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	40	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	40	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	36	50	P	
404191D	SOFT COMPUTING	PP	100	40	23	42	65	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	35	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 991/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383080 LAGAD SHITAL DILIP ASHA ,71639135L ,MITCOE ,B120383080

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	26	56	82	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	45	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	43	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	58	87	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	47	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	54	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	49	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	58	83	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	48	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1152/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383081 LALA RISHAB ROSHAN NEELU ,71542042K ,MITCOE ,B120383081

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	46	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	37	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	44	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	45	71	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	30	50	80	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	41	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	40	63	P	
404191D	SOFT COMPUTING	PP	100	40	28	49	77	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	42	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1156/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383082 LIMAYE ABHISHEK RADHAKRISHNA REVA ,71542044F ,MITCOE ,B120383082

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	53	72	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	43	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	50	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	55	81	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	30	54	84	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	52	79	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	43	70	P	
404191D	SOFT COMPUTING	PP	100	40	29	53	82	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	47	73	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1213/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383083 LOKHANDE SAMPADA MANOJ NALINI ,71542048J ,MITCOE ,B120383083

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	52	76	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	39	59	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	46	74	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	55	82	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	50	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	55	78	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	44	69	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	50	75	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	43	63	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1184/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383084 MAHESHWARI SHREY RAJESH REENA ,71542057H ,MITCOE ,B120383084

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	47	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	36	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	35	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	46	68	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	49	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	46	68	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	48	73	P	P
404191D	SOFT COMPUTING	PP	100	40	23	56	79	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	42	59	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1094/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383085 MAIND SAYALI GOPALRAO SANGITA ,71542058F ,MITCOE ,B120383085

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	38	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	39	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	29	38	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	51	70	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	46	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	52	63	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	48	70	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	48	74	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383086 MANGESH SUNIL RAHANE MEENA ,71542061F ,MITCOE ,B120383086

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	44	68	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	40	55	P
404182	COMPUTER NETWORKS	PP	100	40	18	38	56	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	43	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	27	39	66	P	C	404191D	SOFT COMPUTING	PP	100	40	24	51	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	46	66	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	38	52	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	46	62	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			91	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 1061/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383087 MARAL VRUSHABH SUNIL ASHA ,71639136J ,MITCOE ,B120383087

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	46	69	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	28	42	P
404182	COMPUTER NETWORKS	PP	100	40	15	42	57	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	41	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	40	55	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	24	50	74	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	41	58	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	14	34	48	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	50	71	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			85	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 985/1500 , RESULT:FIRST CLASS

B120383088 MASHALKAR JAYSHREE SUBHASH ANUPAMA ,71639137G ,MITCOE ,B120383088

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	59	79	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	46	61	P
404182	COMPUTER NETWORKS	PP	100	40	11	41	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	45	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	53	72	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	55	77	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	47	58	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	44	60	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	36	54	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			27	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C	404195	PROJECT PHASE II	TW	100	40			91	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			39	P

GRAND TOTAL = 1042/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383089 MHETRE AKSHAY GANESH JAYSHRI ,71542074H ,MITCOE ,B120383089

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	36	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	34	49	P
404182	COMPUTER NETWORKS	PP	100	40	12	33	45	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	39	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	37	56	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	36	49	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	09	41	50	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	29	41	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	28	44	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C	404195	PROJECT PHASE II	TW	100	40			92	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			41	P

GRAND TOTAL = 908/1500 , RESULT:FIRST CLASS

B120383090 NADUVINAMATH SHRUSHTI SHASHIDHARAYYA BRAMARAMBIKA ,71542085C ,MITCOE ,B120383090

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	45	55	P	C	404189	MOBILE COMMUNICATION	PP	100	40	17	39	56	P
404182	COMPUTER NETWORKS	PP	100	40	08	38	46	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	31	46	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	35	51	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	36	51	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	41	54	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	14	28	42	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	34	54	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	TW	100	40			80	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			40	P

GRAND TOTAL = 927/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

B120383092 NAIDU YOGESH SUBRAMANIAN HEMAVATHY ,71542088H ,MITCOE ,B120383092

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	59	77	P	C	404189	MOBILE COMMUNICATION	PP	100	40	15	40	55	P
404182	COMPUTER NETWORKS	PP	100	40	11	42	53	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	46	61	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	52	71	P	C	404191D	SOFT COMPUTING	PP	100	40	16	40	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	48	72	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	36	52	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	49	73	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	TW	100	40			97	P
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			46	P

GRAND TOTAL = 1066/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383093 NAPHADE ALPESH GAJANAN SANGITA ,71542092F ,MITCOE ,B120383093

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	45	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	28	43	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	42	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	42	57	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	38	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	36	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	36	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	44	63	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	07	33\$	40	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			86	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 968/1500 , RESULT:FIRST CLASS [\$ 0.1]

B120383094 NAZARE ESHANK JAYANT RAJASHREE ,71542095L ,MITCOE ,B120383094

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	48	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	45	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	47	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	47	66	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	28	46	74	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	47	69	P	
404191D	SOFT COMPUTING	PP	100	40	24	42	66	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	39	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1121/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383095 NIMHAN SHRIKANT HARISHCHANDRA USHA ,71542104C ,MITCOE ,B120383095

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	50	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	54	81	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	50	78	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	42	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	46	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	48	73	P	
404191D	SOFT COMPUTING	PP	100	40	26	40	66	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	16	34	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1132/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383096 NIRANJAN KUMAR SUNITA ,71542105M ,MITCOE ,B120383096

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	59	76	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	55	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	49	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	55	77	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	23	53	76	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	43	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	54	78	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	59	80	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	30	47	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			84	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1077/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383097 NISHANT SHRIVASTAVA MUKUL ,71542106K ,MITCOE ,B120383097

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	27	59	86	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	60	76	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	54	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	53	76	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	51	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	50	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	49	66	P	
404191D	SOFT COMPUTING	PP	100	40	26	47	73	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	52	75	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1185/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383098 OTARI OMKAR MAKARAND SAYALI ,71542111F ,MITCOE ,B120383098

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	43	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	45	61	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	45	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	40	57	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	41	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	46	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	37	49	P	
404191D	SOFT COMPUTING	PP	100	40	19	36	55	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	33	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1006/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383099 PAGAR BHAGYASHRI DILIP ASHA ,71639138E ,MITCOE ,B120383099

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	46	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	36	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	42	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	45	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	36	54	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	50	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	42	62	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	53	73	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	36	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			88	P	
404195	PROJECT PHASE II	PR	50	20			39	P	

GRAND TOTAL = 1014/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383100 PANDE ANIKET DILIP SAMPRATI ,71542118C ,MITCOE ,B120383100

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	39	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	45	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	50	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	31	44	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	17	41	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	28	43	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	28	49	P	
404191D	SOFT COMPUTING	PP	100	40	21	42	63	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	13	30	43	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			88	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 917/1500 , RESULT:FIRST CLASS

B120383101 PARAB ABHINAV AJIT MANISHA ,71547435K ,MITCOE ,B120383101

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	50	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	48	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	37	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	43	58	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	40	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	38	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	41	63	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	50	67	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	38	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1024/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383102 PARDESHI(AWASTHI) SHRUTI PRAVIN HEMLATA ,71416044L ,MITCOE ,B120383102

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	53	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	51	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	54	80	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	57	80	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	50	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	45	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	44	69	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	50	72	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	36	54	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	P
404195	PROJECT PHASE II	TW	100	40			83	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1115/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383103 PATIL KANCHAN BHIMARAO PRATIBHA ,71542134E ,MITCOE ,B120383103

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	56	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	10	57	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	52	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	49	67	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	50	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	40	56	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	44	61	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	54	75	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	40	60	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	P
404195	PROJECT PHASE II	TW	100	40			86	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383104 PATIL PALLAVI PRAKASH NIRUPAMA ,71542137K ,MITCOE ,B120383104

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	46	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	49	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	43	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	61	82	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	45	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	47	68	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	49	72	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	54	75	P	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	24	40	64	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383105 PATIL RASHMI RAOSAHEB MANGAL ,71639139C ,MITCOE ,B120383105

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	55	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	49	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	55	75	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	37	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	51	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	40	62	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	38	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1067/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383106 PATIL SONALI KUMAR VIJAYA ,71542144B ,MITCOE ,B120383106

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	43	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	45	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	55	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	45	61	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	50	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	46	60	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	49	71	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	47	70	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	33	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1056/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383107 PATOLE UTKARSHA SANJAY LALITA ,71639140G ,MITCOE ,B120383107

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	52	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	49	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	60	77	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	52	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	48	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	45	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	49	69	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	50	70	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	38	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1077/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383108 PAWAR ANAND DILIP VIJAYA ,71416085H ,MITCOE ,B120383108

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	39	51	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	31	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	40	55	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	33	46	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	34	48	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			28	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	33	44	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	31	43	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	28	42	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	29	43	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			88	P	
404195	PROJECT PHASE II	PR	50	20			39	P	

GRAND TOTAL = 835/1500 , RESULT:HIGHER SECOND CLASS

RESULT RESERVED FOR BACKLOGS.

B120383109 PERLA PRADNYA DATTATRAY KAVITA ,71639141E ,MITCOE ,B120383109

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	41	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	43	66	P	
404183	MICROWAVE ENGINEERING	PP	100	40	21	52	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	46	65	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	35	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	46	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	50	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	55	74	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	37	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			78	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1055/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383110 PHAD ANURAG SADASHIV SHAKUNTALA ,71639142C ,MITCOE ,B120383110

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	59	83	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	42	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	61	89	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	60	85	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	25	52	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	49	71	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	49	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	26	57	83	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	28	50	78	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1233/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383111 PHANSE PADMAJA SHRIDHAR BHARATI ,71542163J ,MITCOE ,B120383111

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	53	68	P	C	404189	MOBILE COMMUNICATION	PP	100	40	17	28	45	P	
404182	COMPUTER NETWORKS	PP	100	40	09	38	47	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	33	49	P	
404183	MICROWAVE ENGINEERING	PP	100	40	16	49	65	P	C	404191D	SOFT COMPUTING	PP	100	40	27	36	63	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	54	67	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	35	52	P	
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	36	55	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			93	P	
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 982/1500 , RESULT:FIRST CLASS

B120383112 PRANATI BHANUDAS CHAVAN LATA ,71542172H ,MITCOE ,B120383112

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	40	57	P	C	404189	MOBILE COMMUNICATION	PP	100	40	11	41	52	P	
404182	COMPUTER NETWORKS	PP	100	40	12	32	44	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	43	68	P	
404183	MICROWAVE ENGINEERING	PP	100	40	25	48	73	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	49	63	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	51	59	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	13	38	51	P	
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	12	33	45	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			77	P	
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 955/1500 , RESULT:FIRST CLASS

B120383113 PRANAY DESHMUKH DEEPIKA ,71542173F ,MITCOE ,B120383113

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	34	48	P	C	404189	MOBILE COMMUNICATION	PP	100	40	20	28	48	P	
404182	COMPUTER NETWORKS	PP	100	40	09	34	43	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	38	59	P	
404183	MICROWAVE ENGINEERING	PP	100	40	19	40	59	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	40	60	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	40	58	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	38	58	P	
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	43	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			93	P	
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 985/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383114 PRATIK MANIYAR SUNITA ,71416110B ,MITCOE ,B120383114

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	41	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	44	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	37	53	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	47	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			32	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	37	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	34	54	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	37	60	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	31	48	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 990/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383115 RABBE KISHOREE KUNDLIK GEETA ,71639143M ,MITCOE ,B120383115

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	41	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	33	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	52	74	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	32	48	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	41	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	44	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	40	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	10	--	F	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	34	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			82	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

B120383116 RAUT MAYURI JAYPRAKASH REKHA ,71639144K ,MITCOE ,B120383116

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	58	81	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	53	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	53	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	48	63	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	38	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	42	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	48	62	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	51	73	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	41	56	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			77	P	
404195	PROJECT PHASE II	PR	50	20			35	P	

GRAND TOTAL = 1026/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383117 RAUT SWAROOP AVINASH SANGITA ,71542197C ,MITCOE ,B120383117

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	05	30	35#	P	
404183	MICROWAVE ENGINEERING	PP	100	40	10	35	45	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	06	36	42	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	28	44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			31	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	09	31	40	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	37	53	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	34	53	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	30	42	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			39	P	

GRAND TOTAL = 838/1500 , RESULT:HIGHER SECOND CLASS [# O.4]

B120383118 SAKHARE ARVIND SUBHASH ANJANA ,71639145H ,MITCOE ,B120383118

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	55	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	32	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	46	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	41	61	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	35	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	39	55	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	40	67	P	
404191D	SOFT COMPUTING	PP	100	40	24	40	64	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	33	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1007/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383119 SALONI CHALUMURI LEENA ,71542215E ,MITCOE ,B120383119

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	51	72	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	49	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	56	78	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	50	73	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	42	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	45	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	45	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	49	65	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	21	48	69	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1121/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383120 SALUNKHE SNEHAL DILIP VARSHA ,71639146F ,MITCOE ,B120383120

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	53	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	43	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	59	81	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	47	60	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	42	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	33	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	46	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	38	55	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	18	36	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1016/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383122 SAWANT SWAPNIL SURYAKANT SUPRIYA ,71639147D ,MITCOE ,B120383122

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	50	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	54	73	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	48	67	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	43	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	45	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	49	74	P	
404191D	SOFT COMPUTING	PP	100	40	26	40	66	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	39	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			89	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1106/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383123 SHAIKH ZAINAB BADSHAH TABASSUM ,71542236H ,MITCOE ,B120383123

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	32	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	35	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	51	63	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	43	60	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	38	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	36	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	37	52	P	
404191D	SOFT COMPUTING	PP	100	40	25	34	59	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	21	44	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1016/1500 , RESULT:FIRST CLASS WITH DISTINCTION

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383124 SHASHANK SHEKHAR MANJU ,71542243L ,MITCOE ,B120383124

SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	15	-- F	404189	MOBILE COMMUNICATION	PP	100	40	13	24	-- F
404182	COMPUTER NETWORKS	PP	100	40	10	14	-- F	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	11	-- F
404183	MICROWAVE ENGINEERING	PP	100	40	AA	21	-- F	404191D	SOFT COMPUTING	PP	100	40	13	32	45 P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	AA	28	-- F	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	06	22	-- F
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	AA	23	-- F	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36 P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20		35	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25 P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20		30	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34 P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20		40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			28 P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20		35	P C	404195	PROJECT PHASE II	TW	100	40			88 P
404188	PROJECT PHASE I	OR	50	20		40	P C	404195	PROJECT PHASE II	PR	50	20			44 P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120383125 SHEREKAR ATHARVA ANIL USHA ,71542246E ,MITCOE ,B120383125

SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	35	57 P C	404189	MOBILE COMMUNICATION	PP	100	40	20	40	60 P
404182	COMPUTER NETWORKS	PP	100	40	11	42	53 P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	39	55 P
404183	MICROWAVE ENGINEERING	PP	100	40	20	50	70 P C	404191D	SOFT COMPUTING	PP	100	40	26	41	67 P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	49	68 P C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	14	52	66 P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	38	61 P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39 P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20		42	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40 P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20		39	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42 P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20		40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42 P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20		41	P C	404195	PROJECT PHASE II	TW	100	40			94 P
404188	PROJECT PHASE I	OR	50	20		40	P C	404195	PROJECT PHASE II	PR	50	20			46 P

GRAND TOTAL = 1062/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383126 SHINDE AMEY BALKRISHNA LATA ,71542249K ,MITCOE ,B120383126

SEM.:1								SEM.:2							
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	32	47 P C	404189	MOBILE COMMUNICATION	PP	100	40	13	35	48 P
404182	COMPUTER NETWORKS	PP	100	40	06	36	42 P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	33	46 P
404183	MICROWAVE ENGINEERING	PP	100	40	13	45	58 P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	37	52 P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	35	43 P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	32	46 P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	29	47 P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36 P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20		35	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			25 P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20		32	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			27 P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20		37	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30 P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20		32	P C	404195	PROJECT PHASE II	TW	100	40			91 P
404188	PROJECT PHASE I	OR	50	20		43	P C	404195	PROJECT PHASE II	PR	50	20			39 P

GRAND TOTAL = 856/1500 , RESULT:HIGHER SECOND CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383127 SHINDE SHUBHAM DILIP VEENA ,71542255D ,MITCOE ,B120383127

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	36	56	P	C	404189	MOBILE COMMUNICATION	PP	100	40	14	43	57	P	P
404182	COMPUTER NETWORKS	PP	100	40	09	43	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	40	55	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	53	68	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	44	61	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	43	65	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	19	37	56	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	24	50	74	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40				95	P
404188	PROJECT PHASE I	OR	50	20			43	P	C	404195	PROJECT PHASE II	PR	50	20				44	P

GRAND TOTAL = 1032/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383128 SHIVAM RAJESH BARDE JAISHRI ,71416191J ,MITCOE ,B120383128

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	44	65	P	C	404189	MOBILE COMMUNICATION	PP	100	40	13	53	66	P	P
404182	COMPUTER NETWORKS	PP	100	40	13	42	55	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	42	59	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	56	72	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	45	66	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	23	45	68	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	16	42	58	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	36	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20				36	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				23	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40				88	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20				40	P

GRAND TOTAL = 1000/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383129 SHRUTI SUJAN MUNDE VANMALA SUJAN MUNDE ,71556511H ,MITCOE ,B120383129

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	37	52	P	C	404189	MOBILE COMMUNICATION	PP	100	40	17	49	66	P	P
404182	COMPUTER NETWORKS	PP	100	40	14	36	50	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	15	39	54	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	41	62	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	44	61	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	43	59	P	C	404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	14	46	60	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	30	50	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20				39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40				96	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20				43	P

GRAND TOTAL = 1006/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383130 SHUBHAM ABHAY PATIL ANUJA ,71542261J ,MITCOE ,B120383130

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	33	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	33	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	42	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	49	70	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	39	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	48	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	40	54	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	42	60	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	34	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383131 SHUBHAM RAJ DAYAMANTI ,71542266K ,MITCOE ,B120383131

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	30	44	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	35	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	32	48	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	50	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	45	64	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	46	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	28	51	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	40	56	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	13	38	51	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			38	P	

GRAND TOTAL = 934/1500 , RESULT:FIRST CLASS

B120383132 SIDDHI KATTA KANTA ,71542268F ,MITCOE ,B120383132

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	29	46	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	44	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	49	64	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	40	58	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	44	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	38	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	35	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	40	56	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	21	55	76	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			33	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 992/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383133 SNEHAL SONAWANE VANDANA ,71542271F ,MITCOE ,B120383133

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	28	47	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	40	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	45	62	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	46	69	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			33	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	30	48	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	46	69	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	48	69	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	19	42	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			87	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 994/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383134 SONAWANE SHUBHAM DILIP MANISHA ,71542274L ,MITCOE ,B120383134

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	26	45	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	49	71	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	57	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	58	83	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	36	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	50	73	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	49	76	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	50	75	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	25	52	77	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1167/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383135 SONAWANE SHWETA BALASAHEB SUJATA ,71542275J ,MITCOE ,B120383135

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	39	59	P	C
404182	COMPUTER NETWORKS	PP	100	40	11	46	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	58	78	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	59	78	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	28	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	35	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	40	53	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	43	62	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	24	46	70	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			84	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 1027/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383136 SURYAWANSHI SONAL SUNIL MANGAL ,71639148B ,MITCOE ,B120383136

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	36	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	43	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	50	74	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	40	57	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	17	38	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	40	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	40	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	44	60	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	20	42	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1060/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383137 TANNA SHIVANI JAYESH SHEETAL ,71542297K ,MITCOE ,B120383137

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	32	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	30	45	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	56	78	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	49	75	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	41	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	47	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	39	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	43	61	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	26	42	68	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			93	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1045/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383138 TARGHALE TEJAS SANJAY ASHA ,71639149L ,MITCOE ,B120383138

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	42	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	45	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	51	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	41	56	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	35	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	48	61	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	44	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	45	60	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	41	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1032/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383139 TEJALE PRATIK HIRAMAN PRATIBHA ,71214508H ,MITCOE ,B120383139

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	29	40	P
404182	COMPUTER NETWORKS	PP	100	40	09	31	40	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	39	55	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	32	46	P C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	29	48	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C
404188	PROJECT PHASE I	OR	50	20			40	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	34	52	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	33	58	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	40	56	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	29	42	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404195	PROJECT PHASE II	TW	100	40			90	P
404195	PROJECT PHASE II	PR	50	20			42	P

GRAND TOTAL = 921/1500 , RESULT:FIRST CLASS [\$ 0.1]

B120383140 TERANIKAR TANVEER ASHWINI ASHWINI ,71416239G ,MITCOE ,B120383140

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	31	50	P C
404182	COMPUTER NETWORKS	PP	100	40	21	33	54	P C
404183	MICROWAVE ENGINEERING	PP	100	40	19	34	53	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	40	56	P C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	41	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P C
404188	PROJECT PHASE I	OR	50	20			43	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	38	56	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	39	59	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	45	61	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	23	37	60	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P
404195	PROJECT PHASE II	TW	100	40			96	P
404195	PROJECT PHASE II	PR	50	20			47	P

GRAND TOTAL = 1002/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383141 THAKKAR KRUNAL NAROTTAM KUSUM ,71542303H ,MITCOE ,B120383141

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	29	46	P C
404182	COMPUTER NETWORKS	PP	100	40	08	36	44	P C
404183	MICROWAVE ENGINEERING	PP	100	40	16	46	62	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	48	68	P C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	31	47	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P C
404188	PROJECT PHASE I	OR	50	20			40	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	47	68	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	41	66	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	47	61	P
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	16	38	54	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			31	P
404195	PROJECT PHASE II	TW	100	40			95	P
404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 997/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383142 THOMBRE SUJATA AJINATH PADMINI ,71639150D ,MITCOE ,B120383142

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	34	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	35	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	47	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	47	62	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	46	60	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	44	62	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	44	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	51	71	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	17	44	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1023/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383143 TOPLE TRUPTI VIJAY NALINI ,71639151B ,MITCOE ,B120383143

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	34	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	39	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	51	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	49	66	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	16	29	45	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	45	67	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	45	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	44	63	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	37	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			31	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1000/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120383144 TUSHAR SUNIL SUDRIK VAISHALI ,71542312G ,MITCOE ,B120383144

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	42	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	41	61	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	50	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	56	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	34	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	48	67	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	47	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	47	65	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	52	74	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1098/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383145 VIJAY BHUSHAN ACHANTA KAMESHWARI ACHANTA ,71316118D ,MITCOE ,B120383145

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P	C
404182	COMPUTER NETWORKS	PP	100	40	09	47	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	44	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	38	57	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	33	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	42	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	38	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	44	59	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	40	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			42	P	

GRAND TOTAL = 969/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

B120383146 WALKUNDE SUYASH SANJAY LAXMI ,71639152L ,MITCOE ,B120383146

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	36	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	32	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	47	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	36	51	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	36	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	41	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	39	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	40	53	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	38	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			85	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 972/1500 , RESULT:FIRST CLASS

B120383147 WARKE SIDDHANT VASANT VIDHULA ,71639153J ,MITCOE ,B120383147

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	37	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	41	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	52	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	42	64	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	39	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	44	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	59	84	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	51	74	P	
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	22	54	76	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1124/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120383148 ZANJARE SONALI KAILAS SHARADA ,71505732E ,MITCOE ,B120383148

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	31\$	40	P	404189	MOBILE COMMUNICATION	PP	100	40	15	47	62	P
404182	COMPUTER NETWORKS	PP	100	40	06	34	40	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	29	49	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	38	48	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	39	53	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	36	44	P	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	12	35	47	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	13	51	64	P	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C	404195	PROJECT PHASE II	TW	100	40			88	P
404188	PROJECT PHASE I	OR	50	20			40	P C	404195	PROJECT PHASE II	PR	50	20			40	P

SEM.:2

GRAND TOTAL = 896/1500 , RESULT:HIGHER SECOND CLASS [\$ 0.1]

RESULT RESERVED FOR BACKLOGS.