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Mr. Nitin Paranjpe, Edutech Solutions, Vadodara
Mr. Mangesh Edake, MES, Bangalore
Mr. Ramesh & Mr. Javed, PANTECH Ltd, Pune
Dr. Parikshit Mahalle, SKNCOE, Pune
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Registration Charges:

Academicians/ Research Scholar: Rs.3500/-
Industry Person: Rs.4000/-
PG/UG Students: Rs.1000/-

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Email: events.computer@mitcoe.edu.in

[Last date for registration: 10/12/2016]

Registration is on first come first serve basis.

Maximum Entries: 40

For Queries:

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Registration Form

TWO WEEKS FACULTY DEVELOPMENT PROGRAMME

(14th December to 23rd December 2016)
ON

CODE THE KITS

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MIT College of Engineering, Pune



DEPARTMENT OF COMPUTER ENGINEERING & ELECTRONICS AND TELECOMMUNICATION ENGINEERING

TWO WEEKS FACULTY DEVELOPMENT PROGRAMME

On

CODE THE KITS (SPPU-QIP Sponsored)

In Association with Nvidia GPU Education
Centre, MITCOE

14th December to 23rd December 2016

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ABOUT MAEERS MIT COE

Maharashtra Academy of Engineering & Education Research (MAEER), Pune was established in 1983 and is known for educational excellence. MAEER is multi-disciplinary organization conducting university approved academic programme in Engineering, Management, Pharmacy, Medicine and Polytechnic. More than 55,000 students study in various institutions of MAEER. The student community represents entire cross section of the country. The culture of value based education system is strictly followed to encourage good character building among the young generation. In recognition of the services rendered so far a UNESCO chair in human rights, democracy and peace is constituted at MAEER's MIT. Recently, MAEER's has established MIT-University (MIT Art, Design and Technology). MITCOE is affiliated to University of Pune and is located at the MIT Campus Kothrud, Pune.

ABOUT THE DEPARTMENT

Computer Engineering and E & TC department were started in 2001 and is well equipped with an excellent infrastructure. Both departments offers U.G. (intake 120 students) and for P.G. programme with intake 18 students in Computer Engineering and 36 students intake in E & TC.

ABOUT THE FDP

SPPU university syllabus is a blend of concepts and advances using high end FOSS technologies. Objectives of this syllabus is to encourage students to use High Performance Computing, Embedded System & Kits. On similar line some of the courses like - High Performance Computing, Embedded System, and Kits Programming in practical's were included. Courses are designed to understand and solve real life problems along with need of industry. This FDP, "CODE THE KITS" is designed to learn and understand the depth of Kits Programming. This FDP helps faculties and researchers to understand and imply the knowledge to solve day to day problems using HPC, IOT, CUDA and ES Domain.

FDP OBJECTIVES

- ❑ To arouse the awareness in participants for the need & applications of "HPC, IOT, EMBEDDED KITS", and their use to solve Real Life Problems.
- ❑ To acquaint participants to modern tools, CUDA programming and NS-3 simulator.
- ❑ To provide hands on exposure for kits like Raspberry Pi, Beagle Bone Black and CUDA-TESLA.

FDP CONTENTS

- ❑ Key Notes address from Domain Experts - Academia and Industry.
- ❑ Raspberry Pi Kit - Introduction and Hands-on sessions for different applications.
- ❑ CUDA Programming: Introduction and Applications: Hands-on.
- ❑ TESLA 40 Kit Programming
- ❑ Beagle Bone Black Kit: Introduction and Applications: Hands-on.
- ❑ NS-3 Simulator with hands-on real time applications.

FACULTY DEVELOPMENT PROGRAMME

ON

CODE THE KITS

CHIEF PATRONS

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